



02-C-084

1/2

FIG. 1

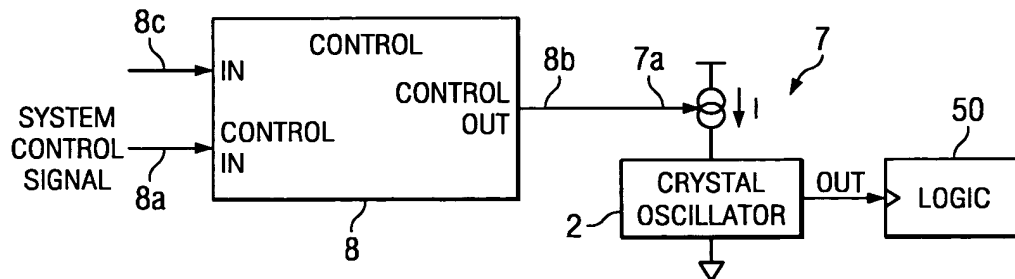
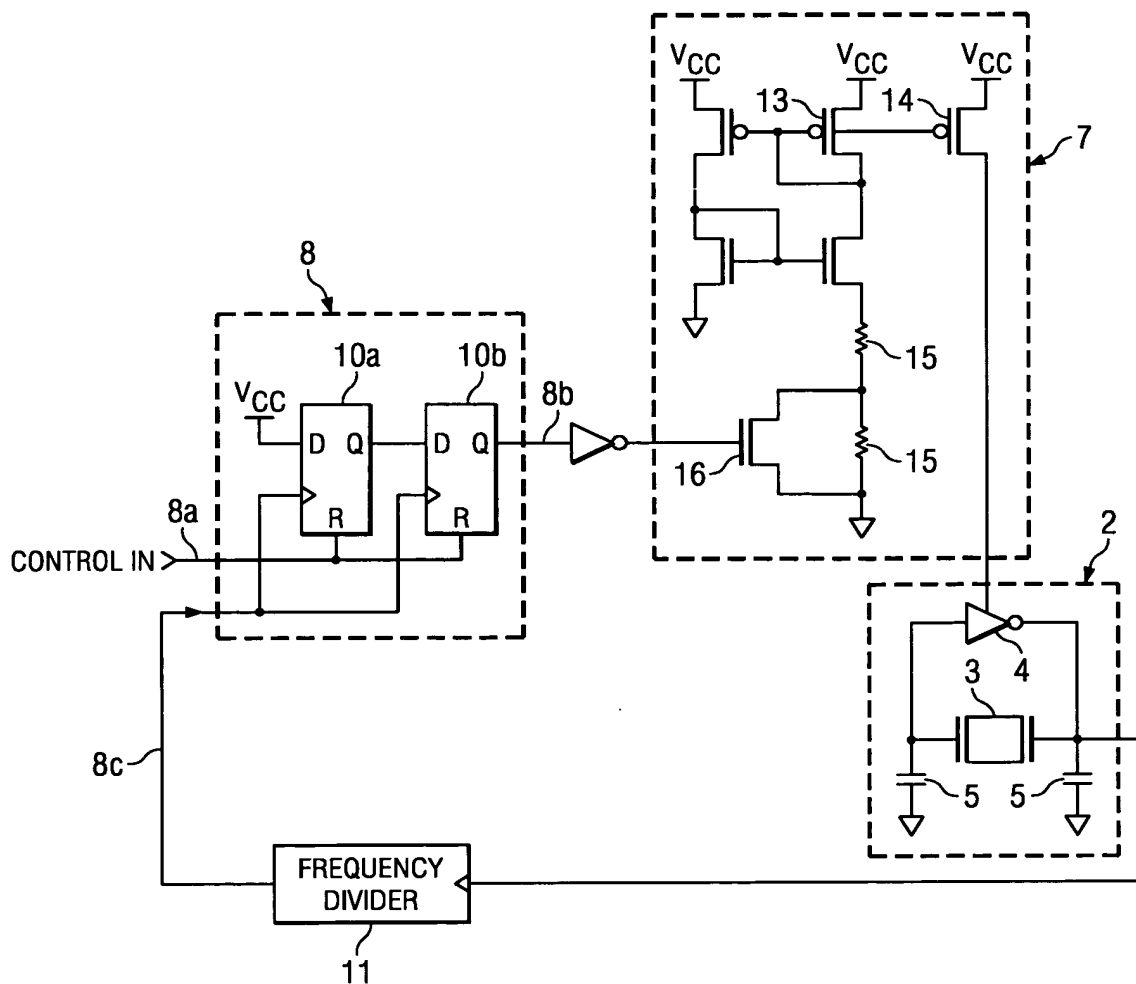


FIG. 2



The diagram shows a circuit labeled 7 enclosed in a dashed box. Inside the box, there is a CRITICAL PATH MONITOR 19, two resistors 18, and a VCC supply. The output of the monitor is labeled "FROM OUTPUT 8b". The circuit is connected to a CRYSTAL OSCILLATOR 2 and a FREQUENCY DIVIDER 11.

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graph TD
    A[POWER-UP SEQUENCE] --> B[PLACE CONTROL INPUT 8a IN INACTIVE STATE (FLIP-FLOPS RESET)]
    B --> C[CONFIGURE CURRENT SOURCE TO SOURCE HIGH CURRENT LEVEL TO CRYSTAL OSCILLATOR CIRCUIT]
    C --> D[GENERATE OSCILLATING SIGNAL AT HIGH CURRENT LEVEL]
    D --> E{END OF POWER-UP SEQUENCE?}
    E -- YES --> F[PLACE CONTROL INPUT 8a IN ACTIVE STATE (FLIP-FLOPS ENABLED TO STORE DATA)]
    E -- NO --> A
    F --> G{PREDETERMINED TIME PERIOD ELAPSED?}
    G -- YES --> H[CONFIGURE CURRENT SOURCE TO SOURCE LOW CURRENT LEVEL TO CRYSTAL OSCILLATOR CIRCUIT]
    G -- NO --> F
  
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